

IPW
Docket No. A3GN2504US

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: David G. Naim

Serial No. 10/826,463

Filed: April 16, 2004

Title: SIGNAL CONDITIONING SYSTEM WITH ADJUSTABLE
GAIN AND OFFSET MISMATCHES

Mail Stop Non-Fee (Information Disclosure Statement)
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97(b)**

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), applicants bring to the attention of the Examiner the documents listed on the attached Form PTO-1449. This Information Disclosure Statement is being filed within three months of the filing of the above referenced application or before the mailing date of a first Office Action on the merits for the above-referenced application.

Applicants respectfully request that the Examiner consider the listed documents and evidence that consideration of relevant portions thereof by making appropriate notations on the attached form.

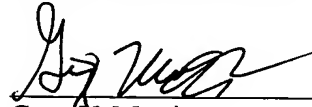
This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art". If the Examiner applies any of the documents as prior art against any claim in the application and applicants determine that the cited documents do not constitute "prior art" under United States law, applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of such documents.

Applicants further reserve the right to take appropriate action to establish the

patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

Respectfully submitted,

May 7, 2004



Greg L. Martinez

Registration No. 53,276

Attorney for Applicant

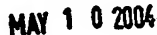
KOPPEL, JACOBS, PATRICK & HEYBL
555 St. Charles Drive, Suite 107
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(805) 373-0060

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as U.S. 1st class mail in an envelope addressed to: Mail Stop Non Fee IDS Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313 on

May 7, 2004
Date

Christina Nicoll
Christina Nicoll



Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449/PTO

(Use as many sheets as necessary)

Sheet	1	of	2
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Complete if Known

Application Number	10/826,463
Filing Date	April 16, 2004
First Named Inventor	David G. Nairn
Art Unit	
Examiner Name	
Attorney Docket Number	A3GN2504US

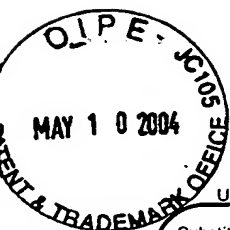
U. S. PATENT DOCUMENTS

[illegible]

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

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Application Number	10/826,463
Filing Date	April 16, 2004
First Named Inventor	David G. Nairn
Art Unit	
Examiner Name	
Attorney Docket Number	A3GN2504US

Sheet	2	of	2
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	CONROY et al, Parallel Pipeline A/D Cohnverter in 1-um CMOS, IEEE Journal of Solid-State Circuits, Vol.28. No. 4, April 1, 1999, P.447-454.	
	2	FU et al, Digital Background Calibration Technique, IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, December 1998, P.1904 - 1991.	
	3	DYER et al, Analog Background Calibration Technique, IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, December 1998, P.1912-1919.	
	4	SUMANEN et al, CMOS Parallel Pipeline A/D Converter, IEEE Journal of Solid-State Circuits, Vol. 36, No. 7, July 2001, P1048-1055.	
	5	HUMMELS et al, Distortion Compensation for Time-Interleaved Analog to Digital Converters, IEEE 1996, P.728-731.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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